

The background features a large, light blue watermark of the Yonsei University logo. The logo is circular with the text 'YONSEI UNIVERSITY' around the top and '1885' at the bottom. In the center is a shield with a book, a lamp, and a central circle.

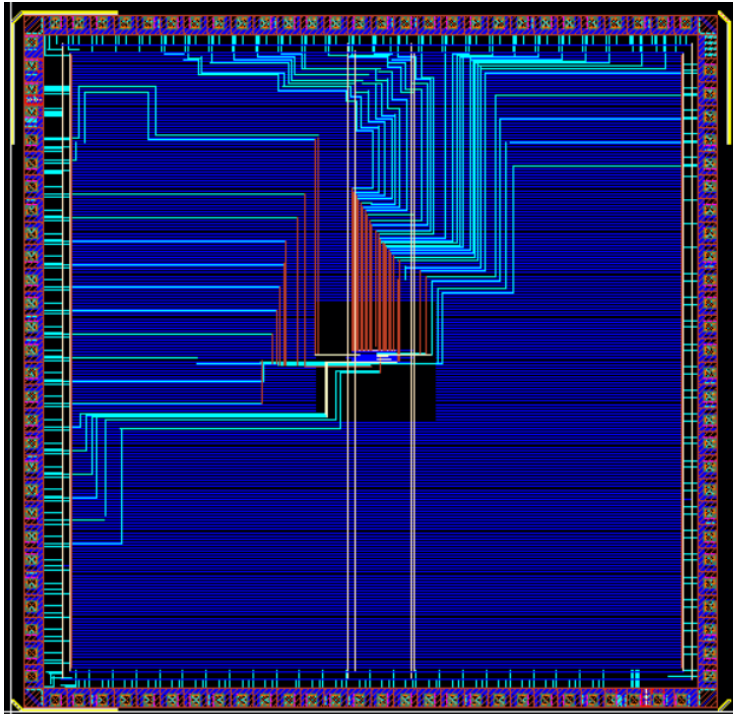
2023 Workshop

Eojin Kim

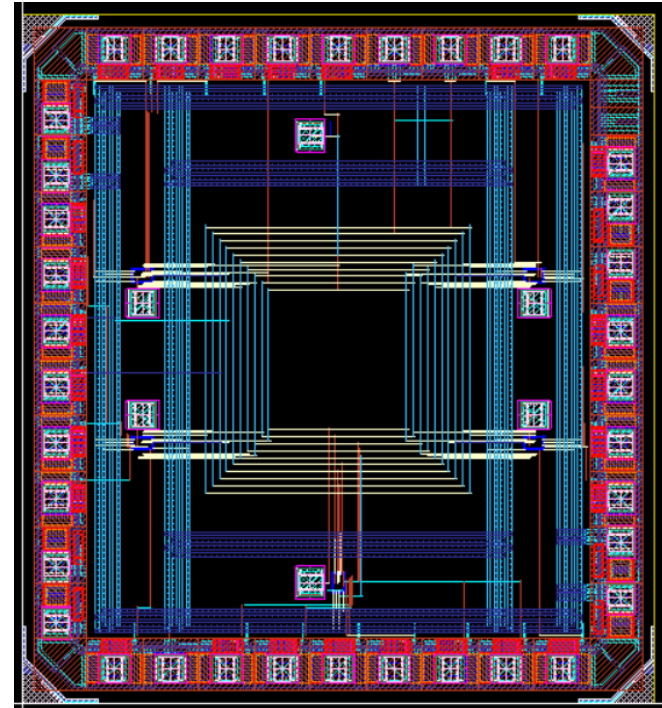
**High-Speed Circuits & Systems Lab.
Dept. of Electrical and Electronic Engineering
Yonsei University**

2023-1 Works

2 Tape-OUT



Vernier Delay Line TDC



SPAD AFE with PHS

2023-2 Plan

- 90nm Chip Measurement Environment Setup (FPGA)
- RTL Design (Using Verilog)

