ISOCC-2010-C-001



## 2010 International SoC Design Conference

First Place

## Best Design Award (Chip Design Contest Session)

presented to

Jinsoo Rhim, Chang-kyung Seong, Wangsoo Kim and Woo-young Choi

Yonsei University, Korea

for

"1.62 and 2.7 Gb/s Dual-rate Adaptive Equalizer"

November 22, 2010

Hi-Seok kim General Chair