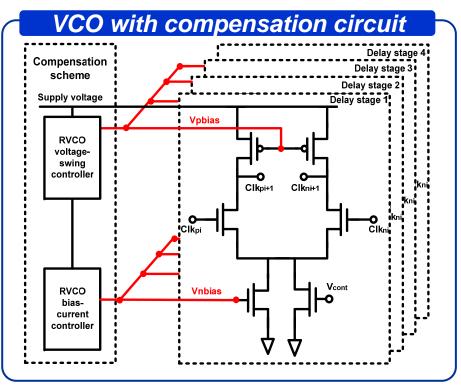
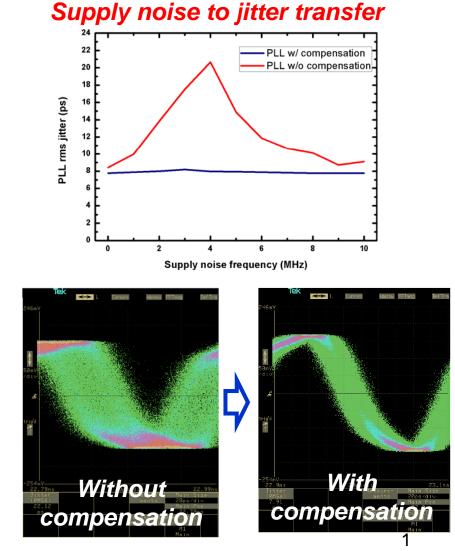


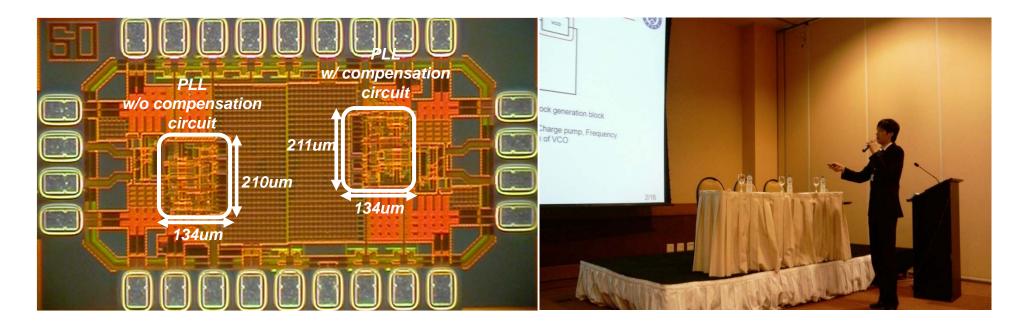
PLL using supply noise and process variation insensitive VCO



High-Speed Circuits and Systems Lab



PVT Insensitive Phase Locked Loop



- The compensation circuit doesn't have area penalty!
- Presented in ISCAS 2011
- Accepted for publication in TCAS II